



PATENTS
Docket No. LT-167

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Gregory P. Dittmer et al.
Application No.: 10/722,808 Confirmation No.: 8187
Filed : November 26, 2003
For : METHODS AND CIRCUITS FOR PROGRAMMABLE
CURRENT LIMIT PROTECTION
Group Art Unit : 2838
Examiner : Jeffrey L. Sterrett
Mail Stop Issue Fee
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In the Notice of Allowability dated September 12, 2005, the Examiner indicated that he could not locate copies of the following references submitted by applicants with the information disclosure statements submitted on August 30, 2004 and September 20, 2004:¹

Foreign Patents

4-101286	09-01-1992	Japan
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¹ The Notice of Allowability stated that the information disclosure statements were submitted on September 1, 2004 and September 22, 2004. However, applicants mailed the stated documents on August 30, 2004 and September 20, 2004 pursuant to 37 C.F.R. § 1.8.

Other Documents

Linear Technology, Nelson., C., "The LT1432:5 Volt Regulator Achieves 90% Efficiency," Linear Technology Magazine, Vol. 2, No. 1, p 12, continued on p 19, 2/92

Maxim Integrated Products, Inc., "MAX635/36/37 Fixed Output CMOS Inverting Switching Regulators," Maxim 1989 Integrated Circuits Data Book, pp. 6-49 to 6-56, 1989

Maxim Integrated Products, Inc., "MAX639 High-Efficiency, +5V Adjustable Step-Down Switching Regulator," Datasheet, 12/91

Maxim Integrated Products, Inc., "MAX783 Triple-Output Power-Supply Controller for Notebook Computers," Datasheet, 05/94

Micro Linear Corporation, "ML4873 Battery Power Control IC," Advance Information Data Sheet, March 15, 1993, pp. 1-8.

Uchida, Takahito, "Control Circuit for Switching Regulator," Japanese Inventor Associated Disclosed Technology Publication No. 92-2362, published 2/15/92 (in Japanese, with translation)

Unitrode, "UCC29421/2, UCC39421/2 Multimode High Frequency PWM Controller," Preliminary Datasheet, 10/1999

Wilcox, M., "The LT1158: Low Voltage, N-Channel Bridge Design Made Easy," Linear Technology Magazine, Vol. 2, No. 1, 2/92

Williams, J., "Basic Principles and Ingenious Circuits Yield Stout Switchers," EDN, 01/18/90

Williams, J., "Refine V/F-converter operation with novel design techniques," EDN, 05/30/85

Accordingly, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants resubmit herewith copies of those references.¹

Also, in the September 20, 2004 information disclosure statement, applicants identified the following four references:

U.S. Patents

5,731,694	03-24-1998	Wilcox et al.
5,994,885	11-30-1999	Wilcox et al.
6,304,066	10-16-2001	Wilcox et al.
6,580,258	06-17-2003	Wilcox et al.

Due to inadvertent error on applicants' part, applicants unintentionally left these references off of the FORM PTO-1449 accompanying the September 20, 2004 information disclosure statement. However, applicants had identified two of these four references (i.e., U.S. Patent Nos. 5,731,694 and 5,994,885) previously in the August 30, 2004 information disclosure statement and accompanying FORM PTO-1449. Accordingly, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants resubmit herewith U.S. Patent Nos. 6,304,066 and 6,580,258 for the Examiner's consideration. Applicants apologize for the error and any inconvenience this has caused.

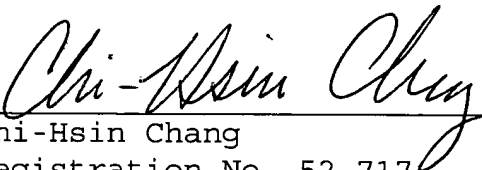
¹ Applicants' submission of this statement is not an admission that the references herein are, or are considered to be, material to patentability of any presented claim. With respect to cited documents other than patents, Applicants have identified dates or possible date codes that appear on the documents. Applicants' identification of these dates is not an admission that the documents were published by or on the dates identified. Applicants reserve the right to challenge the status of any of the cited documents and information as prior art.

Pursuant to the PTO's waiver of the requirement under 37 CFR 1.98 (a)(2)(i), 1276 OG 55, applicants have not submitted copies of each cited U.S. patent and each U.S. patent application publication. Copies of the aforementioned foreign patent publications and other documents, which are listed on the accompanying Form PTO/SB/08, are enclosed herewith.

It is respectfully requested that these documents be (1) fully considered by the Patent and Trademark Office during the examination of this application; and (2) printed on any patent that may issue on this application. Applicants request that a copy of Form PTO/SB/08, as considered and initialed by the Examiner, be returned with the next communication.

An early and favorable action is respectfully requested.

Respectfully submitted,


Chi-Hsin Chang
Registration No. 52,717
Attorney for Applicants

Mark D. Rowland
Registration No. 32,077
Attorney for Applicants

FISH & NEAVE IP GROUP
ROPES & GRAY LLP
Customer No. 1473
1251 Avenue of the Americas
New York, New York 10020-1104
Tel.: (650) 617-4000 (CA)
Fax: (212) 596-9090 (NY)

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

1

of

2

Application Number

10/722.808

Filing Date

November 26, 2003

First Named Inventor

Gregory P. Dittmer

Art Unit

2838

Examiner Name

Jeffrey L. Sterrett

Attorney Docket Number

LT-167

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		4-101286	09-01-1992	Japan		

Examiner
Signature

| Date

Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

Substitute for form 1449A/PTO <h2 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center;">(Use as many sheets as necessary)</p>		Complete if Known	
Sheet 2 of 2		Application Number	10/722,808
		Filing Date	November 26, 2003
		First Named Inventor	Gregory P. Dittmer
		Art Unit	2838
		Examiner Name	Jeffrey L. Sterrett
		Attorney Docket Number	LT-167

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Linear Technology, Nelson., C., "The LT1432:5 Volt Regulator Achieves 90% Efficiency," Linear Technology Magazine, Vol. 2, No. 1, p 12, continued on p 19, 2/92	
		Maxim Integrated Products, Inc., "MAX635/36/37 Fixed Output CMOS Inverting Switching Regulators," Maxim 1989 Integrated Circuits Data Book, pp. 6-49 to 6-56, 1989	
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		Maxim Integrated Products, Inc., "MAX783 Triple-Output Power-Supply Controller for Notebook Computers," Datasheet, 05/94	
		Micro Linear Corporation, "ML4873 Battery Power Control IC," Advance Information Data Sheet, March 15, 1993, pp. 1-8.	
		Uchida, Takahito, "Control Circuit for Switching Regulator," Japanese Inventor Associated Disclosed Technology Publication No. 92-2362, published 2/15/92 (in Japanese, with translation)	
		Unitrode, "UCC29421/2, UCC39421/2 Multimode High Frequency PWM Controller," Preliminary Datasheet, 10/1999	
		Wilcox, M., "The LT1158: Low Voltage, N-Channel Bridge Design Made Easy," Linear Technology Magazine, Vol. 2, No. 1, 2/92	
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Examiner Signature	Date Considered
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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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